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CONFIRMATION NO. 9192

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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a 371 of PCT/JP03/08786 07/10/2003

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 3	TOTAL CLAIMS 4	INDEPENDENT CLAIMS 2
Verified and Acknowledged <i>Dharti Patel</i> Examiner's Signature	<i>DHP</i> Initials			

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## TITLE

Method of protecting semiconductor device and protection apparatus for semiconductor device using the same

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